

35678 U.S. PTO

09/522026



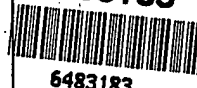
10/08/98

257	697	Class	Subclass	ISSUE CLASSIFICATION
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PATENT NUMBER

6483183



6483183

U.S. UTILITY Patent Application

O.I.P.E. <i>FAZ</i>	PATENT DATE
SCANNED <i>TR3</i> Q.A. <i>Am</i>	NOV 19 2002

APPLICATION NO. 09/522026	CONT/PRIOR D	CLASS 257	SUBCLASS 697	ART UNIT 2822	EXAMINER Potter
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APPLICANTS
Scott Fink
Gregory Bingham
Richard Hull
Scott Ellison

TITLE
Integrated circuit (IC) package with a microcontroller having an n-bit bus and up to n-pins coupled to the microcontroller

PTO-2040
12/98

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
257	697	257	697				
INTERNATIONAL CLASSIFICATION							
H101L		23/48					

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10/11/02 Formal Drawings (1 shts) set

10/8/98

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. 1	Figs. Drwg. 1	Print Fig. 1	Total Claims 34	Print Claim for O.G. 1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	Roy Potter Primary Examiner Technology Center 2800 6/15/02 (Primary Examiner) _____ (Date) _____			6/17/02	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date) _____			ISSUE FEE	
				Amount Due 280	Date Paid 7/1/02
				ISSUE BATCH NUMBER	

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ISSUE FEE IN FILE

Formal Drawings (1 shts) set

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